

CMSC 412 Midterm #2 (Spring 2016)

Name _____

Signature _____

- (1) This exam is closed book, closed notes, and closed neighbor. No calculators are permitted. Violation of any of these rules will be considered academic dishonestly.
- (2) You have 70 minutes to complete this exam. If you finish early, you may turn in your exam at the front of the room and leave. However if you finish during the last ten minutes of the exam please remain seated until the end of the exam so you don't disturb others. Failure to follow this direction will result in points being deducted from your exam.
- (3) Write all answers on the exam. If you need additional paper, I will provide it. Make sure your name is on any additional sheets.
- (4) Partial credit will be given for most questions assuming I can figure out what you were doing.
- (5) Please write neatly. Print your answers if your handwriting is hard to read. If you write something, and wish to cross it out, simply put an X through it. Please indicate if your answer continues onto another page.
- (6) Cell phones must be turned off (not just vibrate) during the exam. A cell phone ringing during the exam will result in 10 points being deducted from your score.

Question	Possible	Score
1	20	
2	20	
3	20	
4	20	
5	20	
Total	100	

1.) (20 points) Define and explain the following terms:

a) Negative ACL

b) Set uid bit

c) Bankers Algorithm

d) Dirty bit

2.) (20 points) Memory Systems

- a) (8 points) Consider an architecture with a two-level page table, but without a TLB. Accessing a memory location takes 100ns. Accessing a disk block takes an average of 10ms. What percentage of accesses can result in page faults if the average access time needs to be less than 350ns?
- b) (6 points) Why is having a processor that supports a single “large reach” map of an arbitrary numbers of page frames from a contiguous region of physical memory to a virtual range useful? Explain what performance gains are possible, and one type of application that can use this type of mapping?
- c) (6 points) Name two parts of the OS kernel that can't be made pageable

3.) (20 Points) Synchronization: Use binary semaphores to implement a solution to the standard readers/writers problem (i.e. readers preferred) that allows either a single writer or at most five readers at a time. Show all variable and semaphore declarations and initial values.

