Introduction to Parallel Computing (CMSC416 / CMSC818X)



Performance Issues



Abhinav Bhatele, Department of Computer Science

Announcements

- Assignment 3 is posted and due on Nov 3
- Final exam: Dec 15 8:00-10:00 AM



Abhinav Bhatele (CMSC416 / CMSC818X)



Performance metrics

- Time to solution
- Time per step (iteration)
- Science progress (figure of merit per unit time)
- Floating point operations per second (flop/s)
- When comparing multiple data points:
 - Speedup, efficiency





Abhinav Bhatele (CMSC416 / CMSC818X)

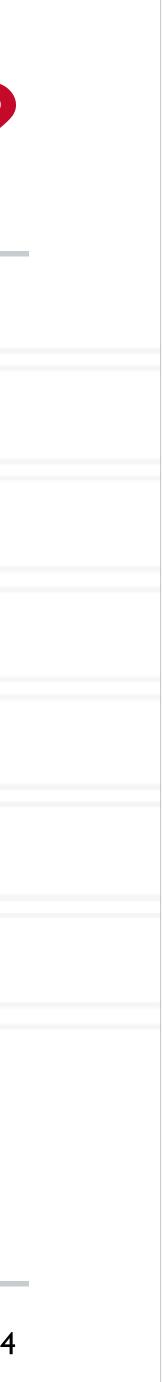


What is the best performance we can get?

- Peak flop/s
- Peak memory bandwidth
- Peak network bandwidth
- Why do we not achieve peak performance?



Abhinav Bhatele (CMSC416 / CMSC818X)



What is happening in a program

- Integer operations
- Floating point operations
- Conditional instructions (branches)
- Loads/stores
- Data movement across the network (messages + I/O)



Abhinav Bhatele (CMSC416 / CMSC818X)



Performance issues

- Sequential performance issues
 - Inefficient memory access: data movement in the memory hierarchy
 - Inefficient floating point operations
- Load imbalance
 - Some processes doing more work than most
- Communication issues / parallel overhead
 - Spending increasing proportion of time on communication
- Insufficient parallelism





Abhinav Bhatele (CMSC416 / CMSC818X)

LIVE RECORDING

6

Performance issues

- Algorithmic overhead / replicated work
 - More computation when running in parallel (e.g. prefix sum)
- Speculative loss
 - Perform extra computation speculatively but not use all of it for the result
- Critical paths
 - Dependencies between computations spread across processes / threads
- Bottlenecks
 - Serial bottlenecks: one process doing some computation and holding everyone up



Abhinav Bhatele (CMSC416 / CMSC818X)







Sequential performance issues

- Identify issues using performance tools
- Solutions:
 - Minimize data movement
 - Data reuse
 - Optimize floating point calculations



Abhinav Bhatele (CMSC416 / CMSC818X)



Communication performance

- Overhead and grainsize (Lots of tiny messages or a few very large messages)
- No overlap between communication and computation
- Increasing amounts of communication as we run on more processes
- Frequent global synchronization



Abhinav Bhatele (CMSC416 / CMSC818X)



Critical paths

- A long chain of dependencies across processes
- We want to identify and avoid having long critical paths
- Solutions:
 - Eliminate completely if possible
 - Shorten the critical path
 - Reduce time spent in a path by removing work on the critical path



Abhinav Bhatele (CMSC416 / CMSC818X)



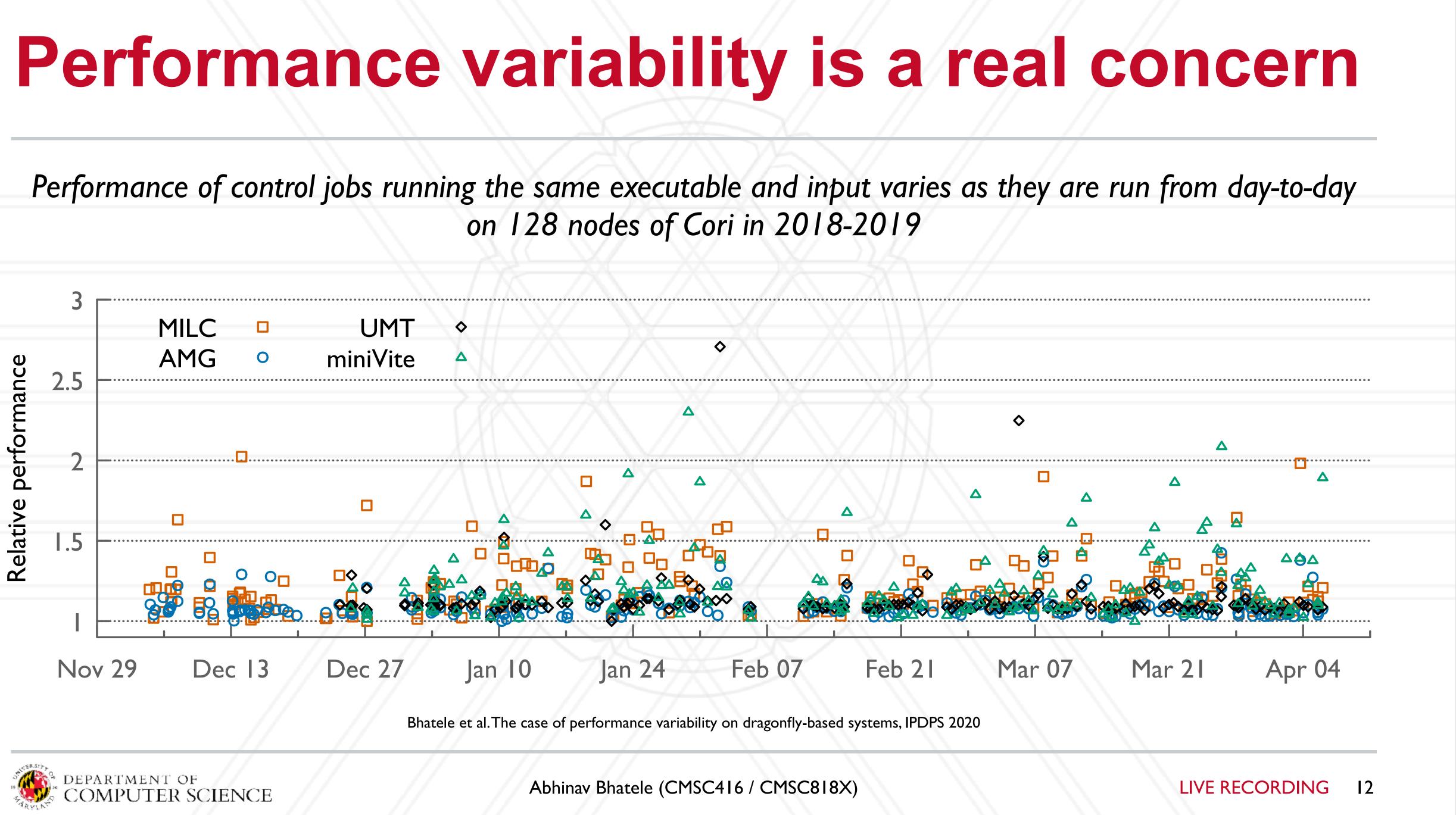
Bottlenecks

- Detect bottlenecks
 - One process busy while all others wait
- Examples:
 - Reduce to one process and then broadcast
 - One process responsible for input/output
 - One process responsible for assigning work to others
- Solutions:
 - Parallelize as much as possible, use hierarchical schemes



Abhinav Bhatele (CMSC416 / CMSC818X)

on 128 nodes of Cori in 2018-2019





Leads to several problems ...

Individual jobs run slower:

- More time to complete science simulations
- Increased wait time in job queues
- Inefficient use of machine time allocation
- Overall lower throughput
- Increased energy usage/costs



Abhinav Bhatele (CMSC416 / CMSC818X)



Affects software development cycle

- Debugging performance issues
- Quantifying the effect of various software changes on performance
 - Code changes
 - System software changes
- Estimating time for a batch job or simulation



Abhinav Bhatele (CMSC416 / CMSC818X)



Sources of performance variability

- Operating system (OS) noise/jitter
- Contention for shared resources
 - Network
 - Filesystem



Abhinav Bhatele (CMSC416 / CMSC818X)

LIVE RECORDING

15

Operating System

- Node on an HPC cluster may have:
 - A "full" linux kernel, or
 - A light-weight kernel
- Decides what services/daemons run
- Impacts performance predictability



Abhinav Bhatele (CMSC416 / CMSC818X)

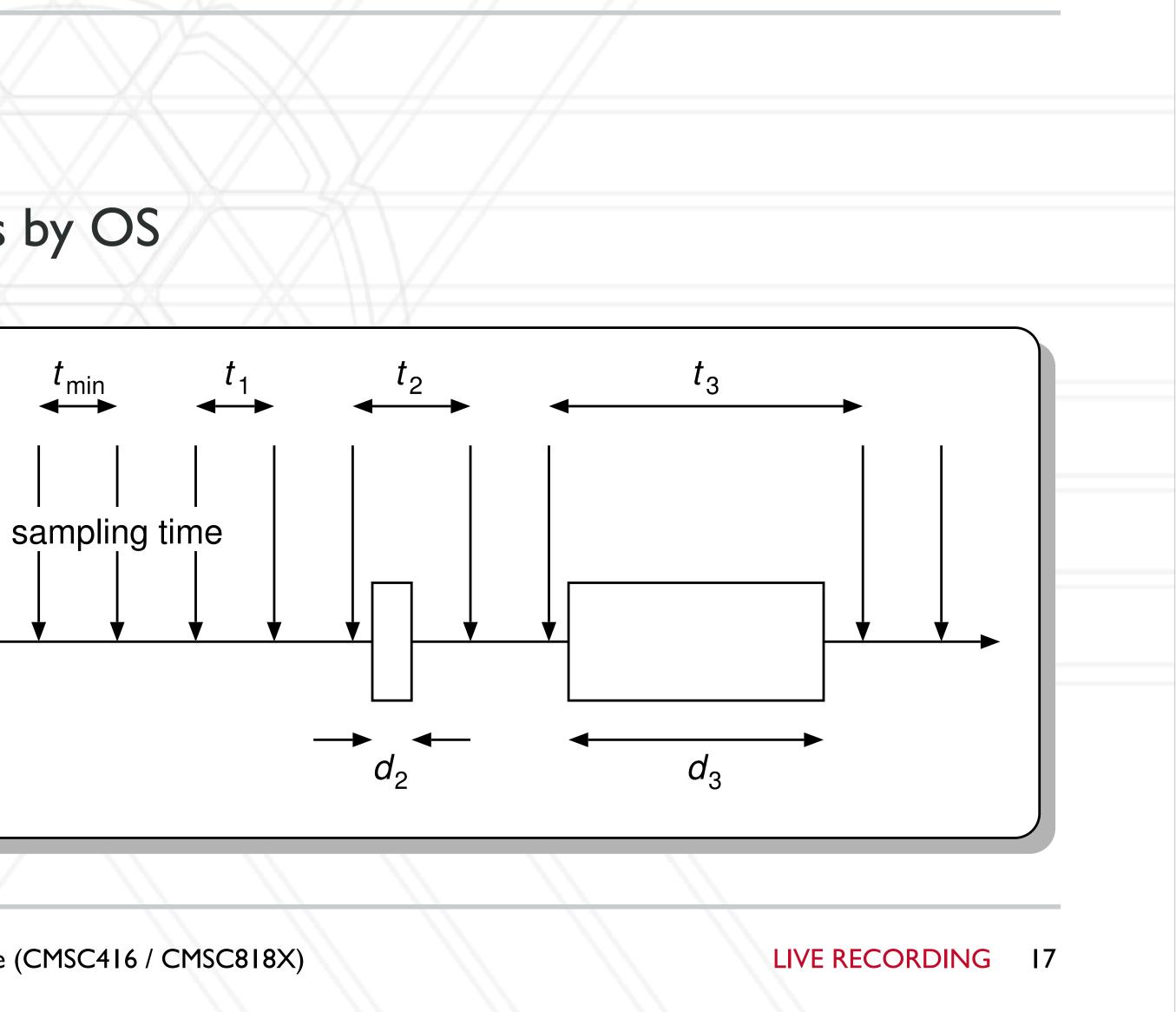


Operating System (OS) Noise

- Also called "jitter"
- Impacts computation due to interrupts by OS

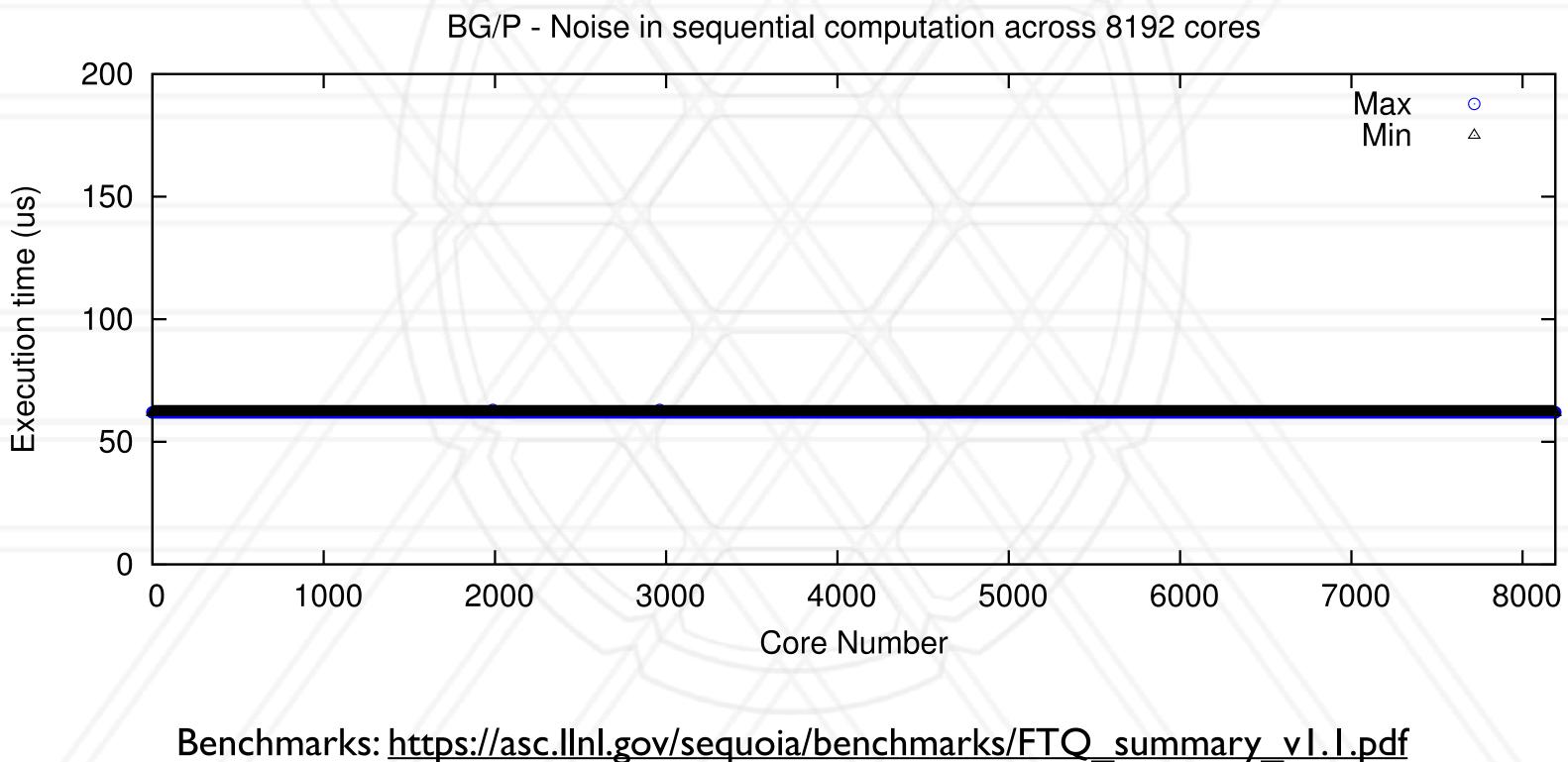


Abhinav Bhatele (CMSC416 / CMSC818X)



Measuring OS Noise

Fixed Work Quanta (FTW) and Fixed Time Quanta (FTQ)





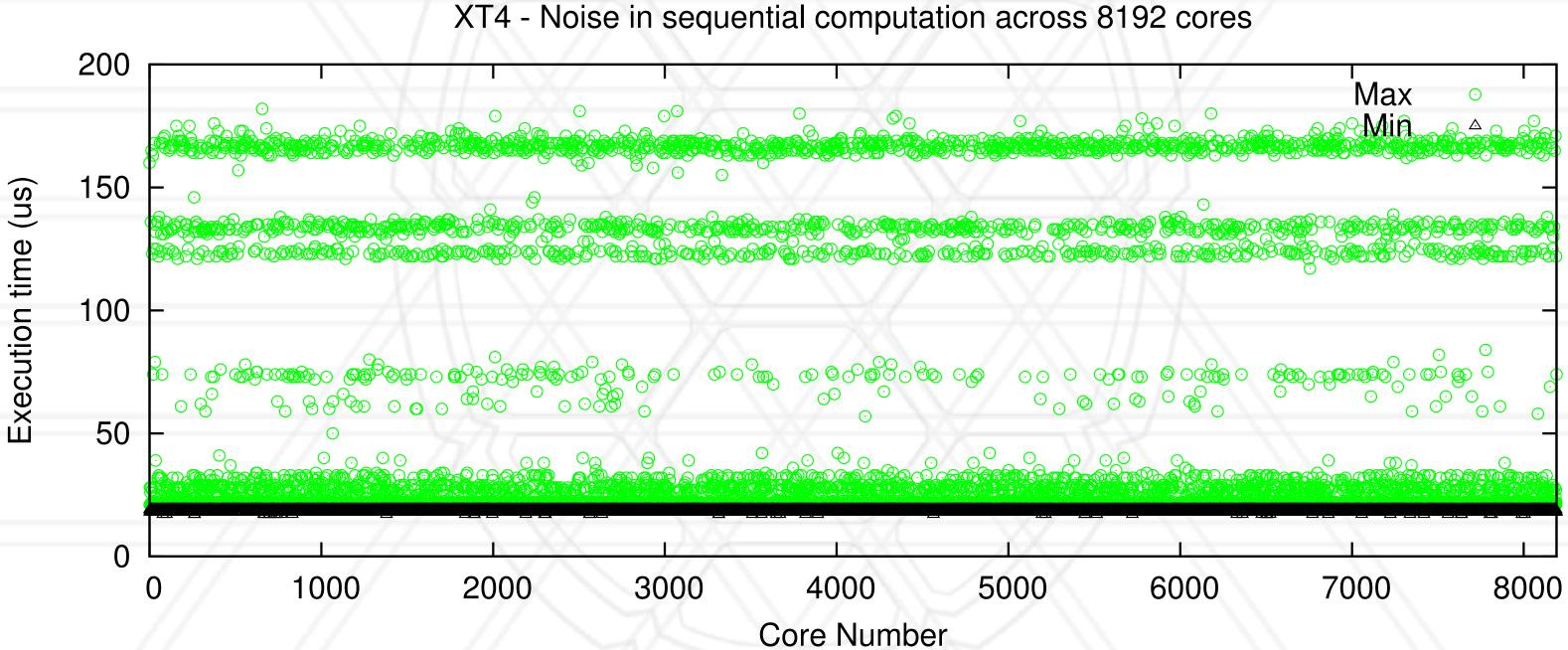
Abhinav Bhatele (CMSC416 / CMSC818X)





Measuring OS Noise

Fixed Work Quanta (FTW) and Fixed Time Quanta (FTQ)



Benchmarks: <u>https://asc.llnl.gov/sequoia/benchmarks/FTQ_summary_v1.l.pdf</u>

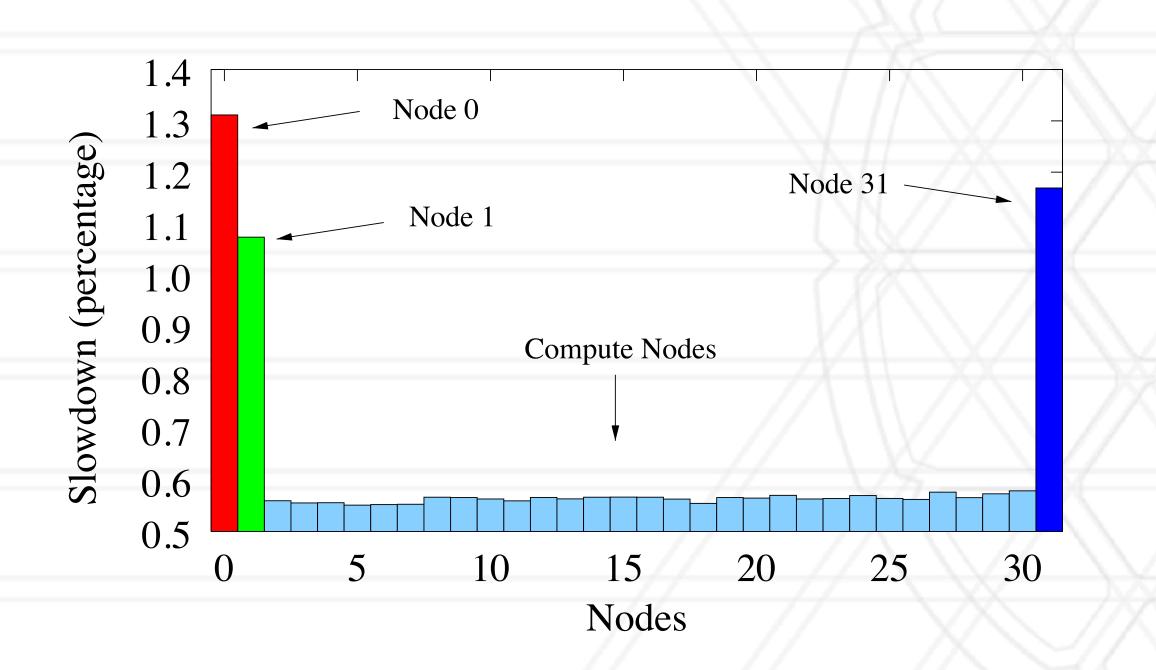


Abhinav Bhatele (CMSC416 / CMSC818X)





The Case of the Missing Supercomputer Performance



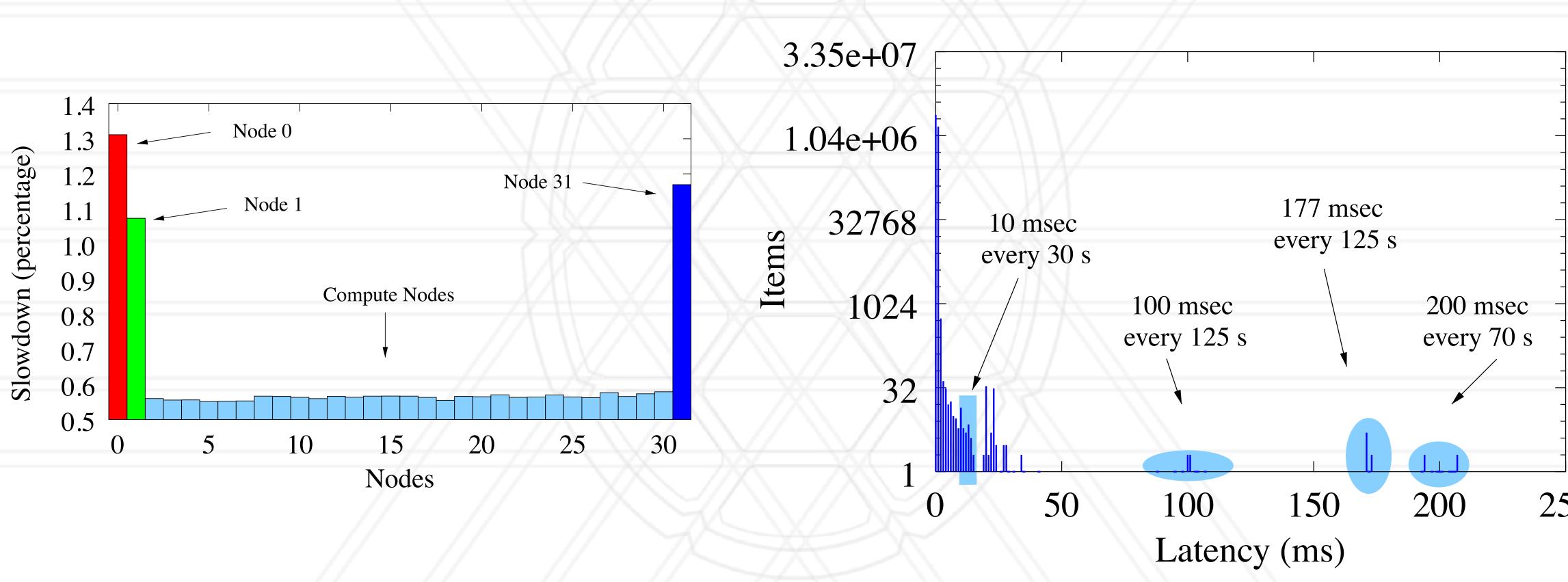
Fabrizio Petrini, Darren J. Kerbyson, and Scott Pakin. 2003. The Case of the Missing Supercomputer Performance: Achieving Optimal Performance on the 8,192 Processors of ASCI Q. In Proceedings of the 2003 ACM/IEEE conference on Supercomputing (SC '03). Association for Computing Machinery, New York, NY, USA, 55. DOI:https://doi.org/10.1145/1048935.1050204



Abhinav Bhatele (CMSC416 / CMSC818X)



The Case of the Missing Supercomputer Performance



Fabrizio Petrini, Darren J. Kerbyson, and Scott Pakin. 2003. The Case of the Missing Supercomputer Performance: Achieving Optimal Performance on the 8,192 Processors of ASCI Q. In Proceedings of the 2003 ACM/IEEE conference on Supercomputing (SC '03). Association for Computing Machinery, New York, NY, USA, 55. DOI:https://doi.org/10.1145/1048935.1050204



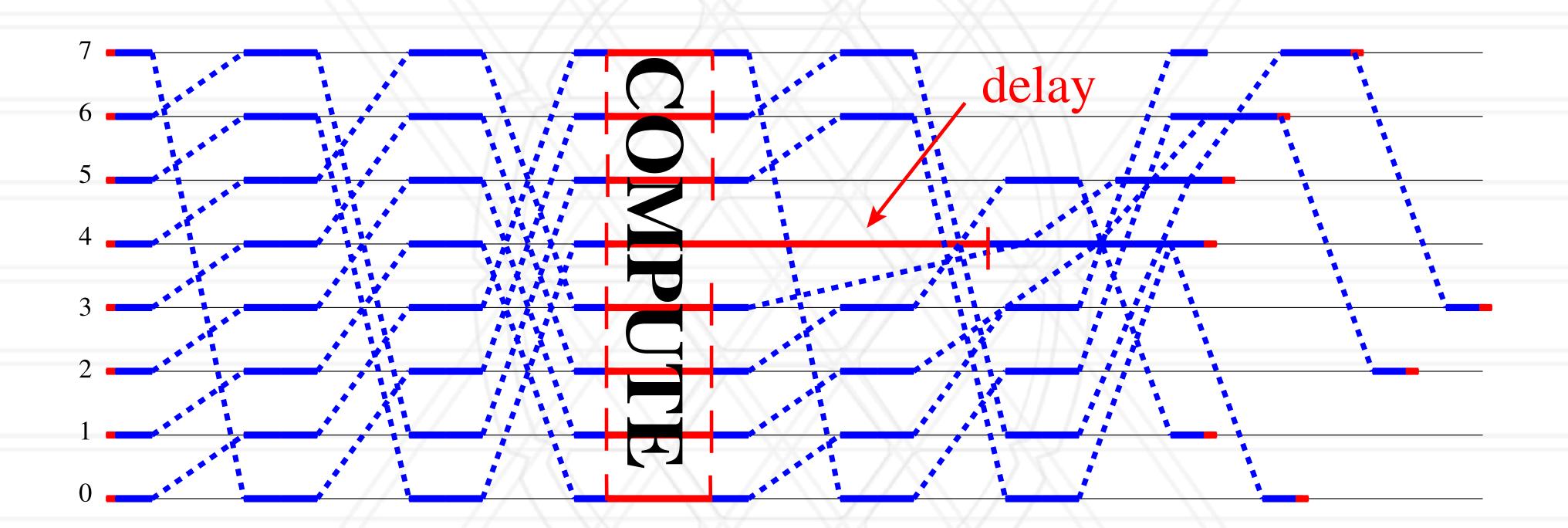
Abhinav Bhatele (CMSC416 / CMSC818X)

LIVE RECORDING

7					
-					
-					
-					
-					
-					
-					
-					
-					
-					
J					
50					

19

Impact on communication



Hoefler et al.: <u>https://htor.inf.ethz.ch/publications/img/hoefler-noise-sim.pdf</u>



Abhinav Bhatele (CMSC416 / CMSC818X)



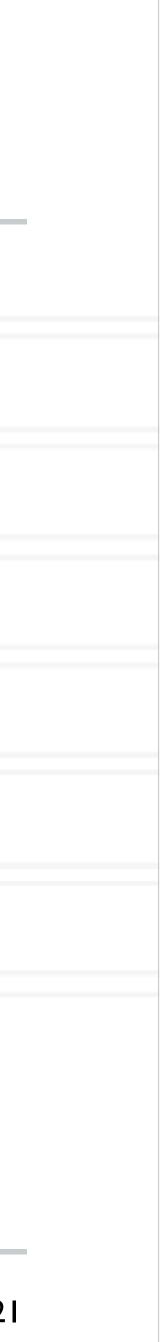
Mitigating OS noise

- Running a light-weight OS
- Turn off unnecessary daemons
- Reduce the frequency of daemons
- Dedicated cores for OS daemons
- User programs can avoid using certain cores





Abhinav Bhatele (CMSC416 / CMSC818X)



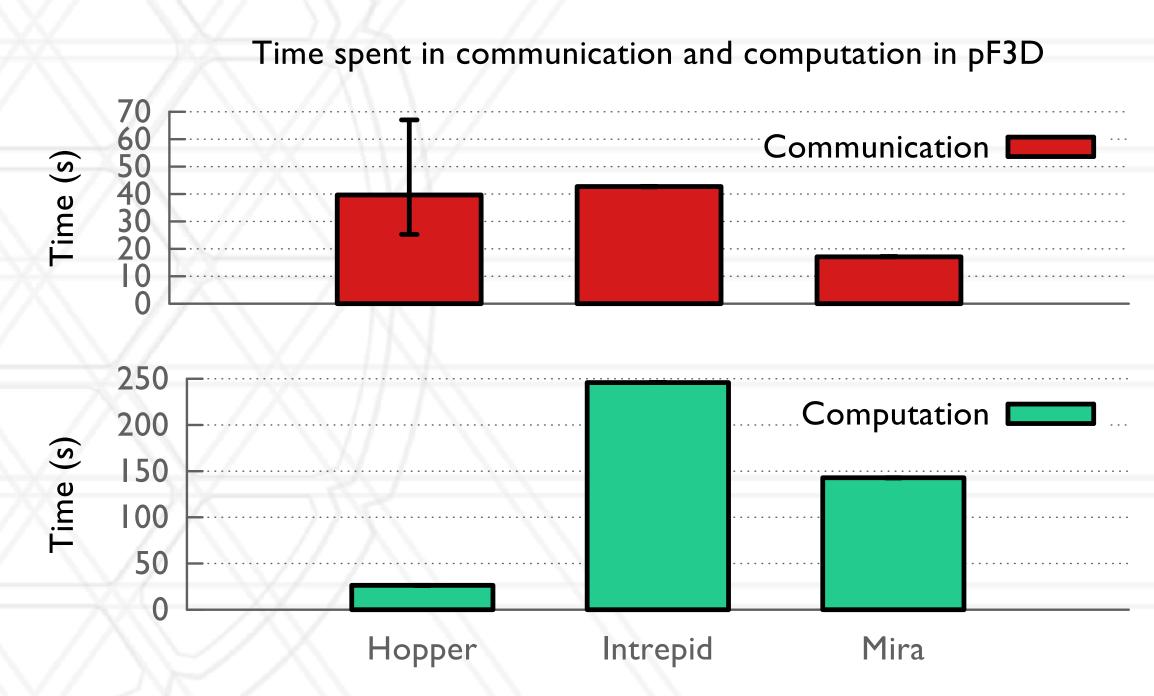
Performance variability due to congestion

- No variability in computation time
- All of the variability can be attributed to communication performance
- Factors:
 - Placement of jobs
 - Contention for network resources

Bhatele et al. http://www.cs.umd.edu/~bhatele/pubs/pdf/2013/sc2013a.pdf

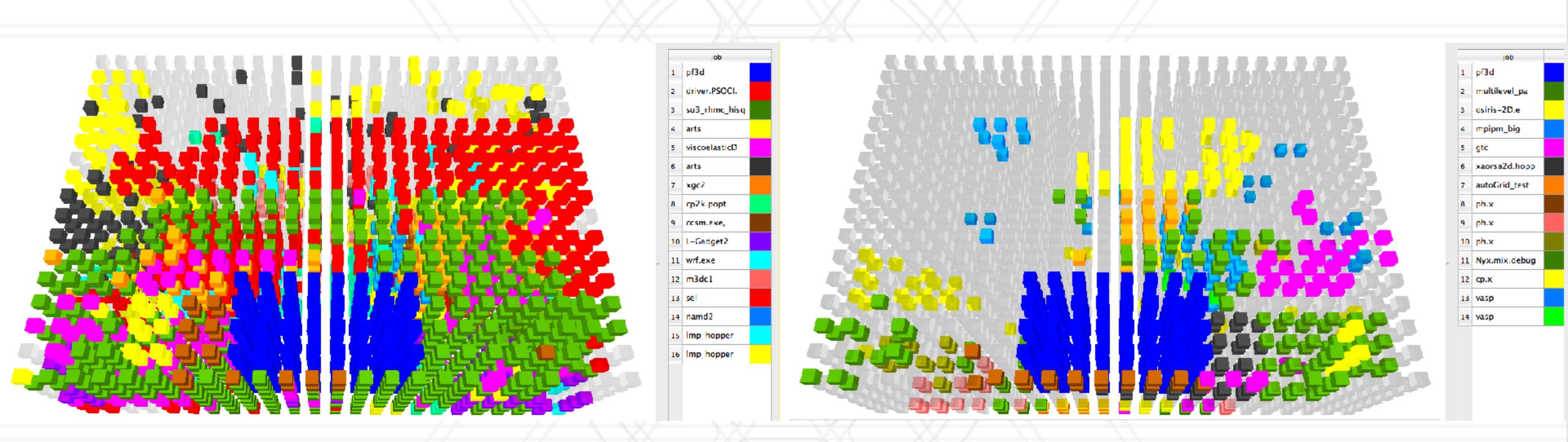


Abhinav Bhatele (CMSC416 / CMSC818X)





Impact of other jobs



April II

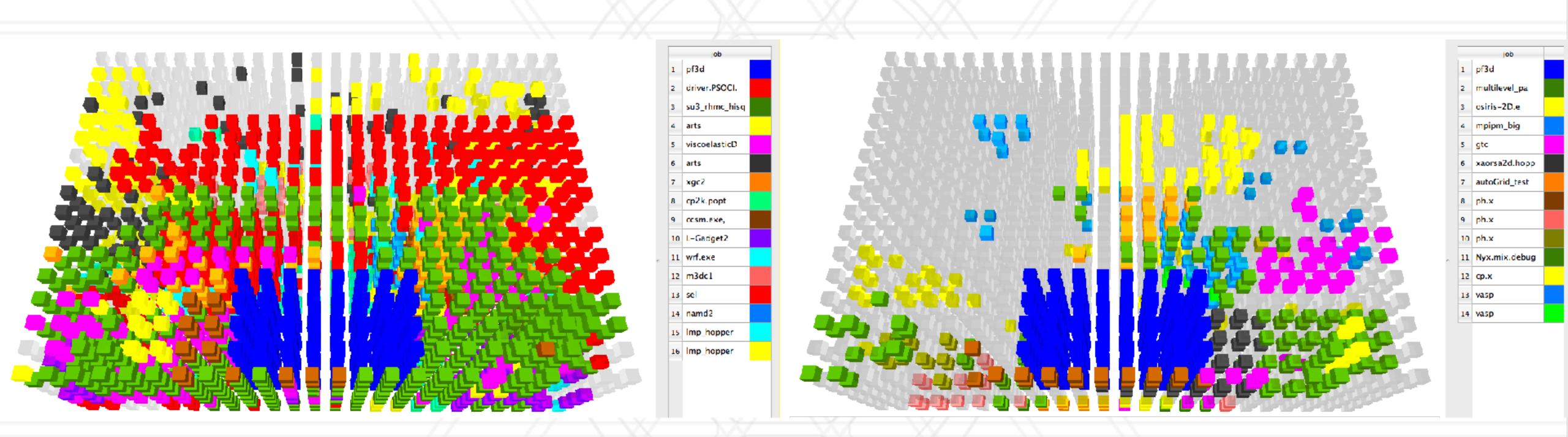


Abhinav Bhatele (CMSC416 / CMSC818X)



April 16

Impact of other jobs



April II MILC job in green



Abhinav Bhatele (CMSC416 / CMSC818X)



April 16 25% higher messaging rate

UNIVERSITY OF MARYLAND

Abhinav Bhatele 5218 Brendan Iribe Center (IRB) / College Park, MD 20742 phone: 301.405.4507 / e-mail: bhatele@cs.umd.edu

